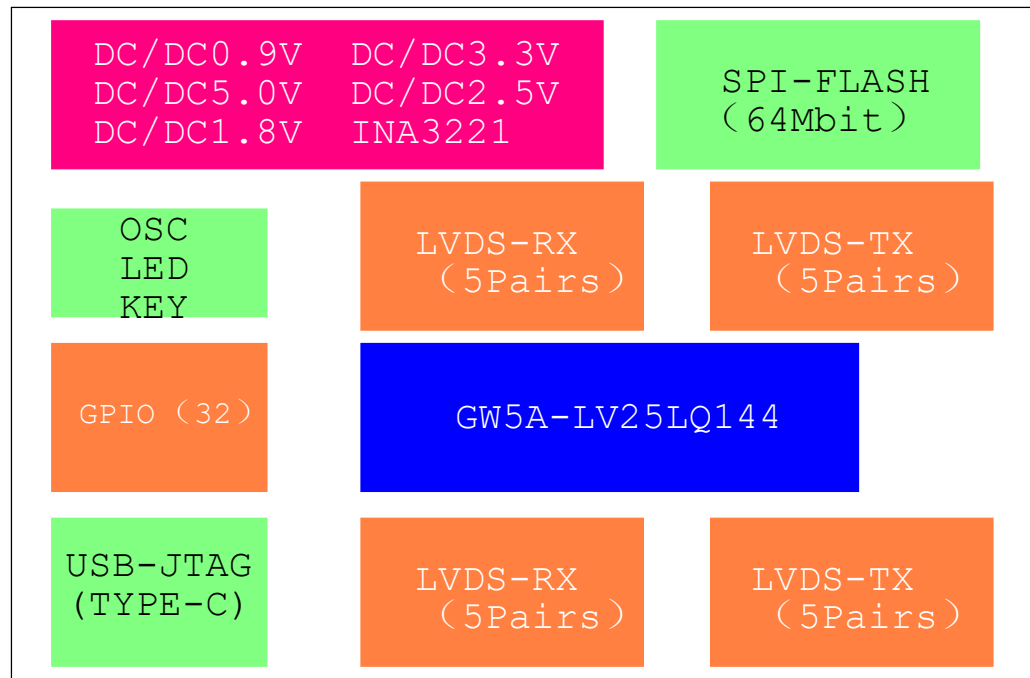


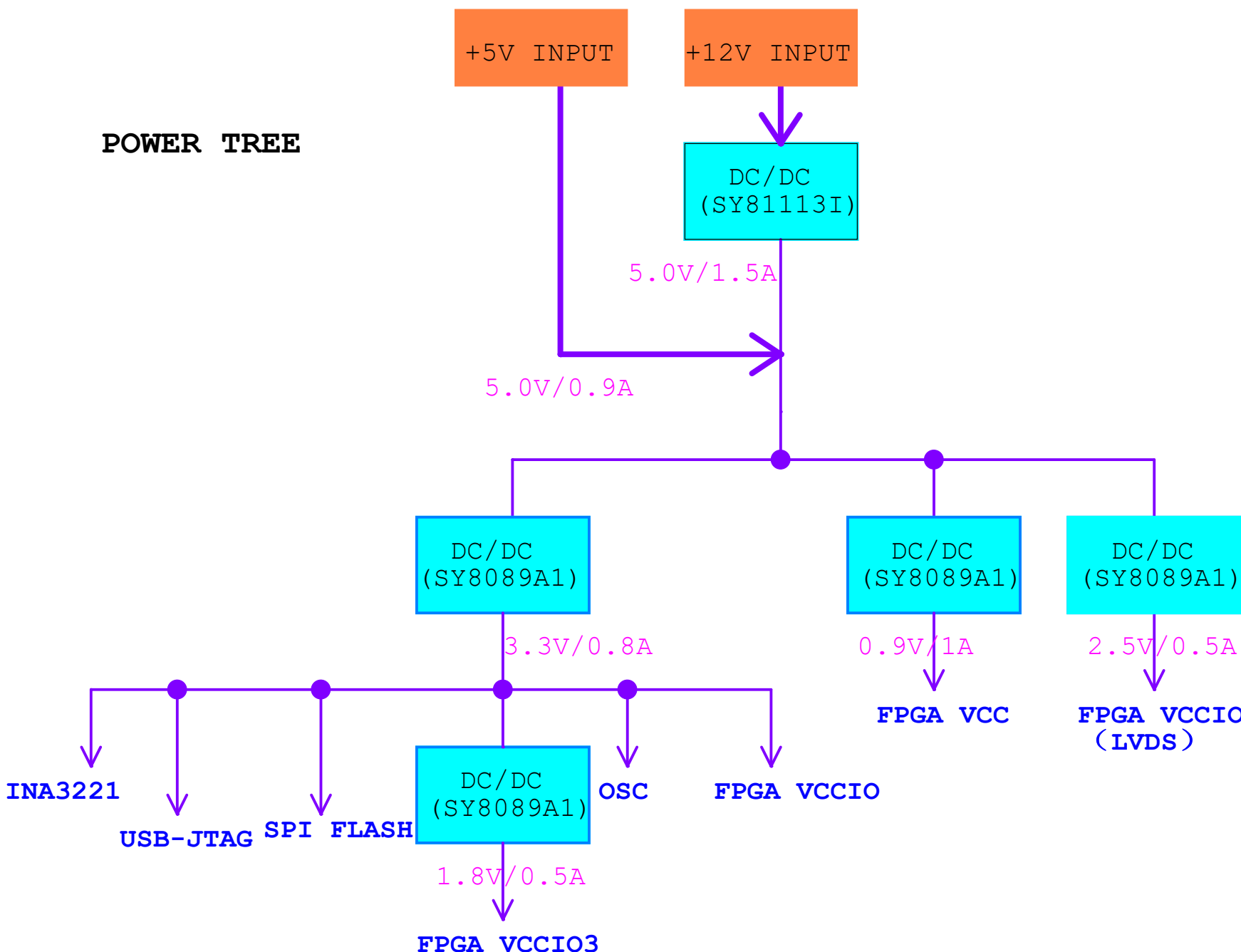
PCB CONTENT

SCHEMATIC	CONTENT	PAGE
COVER PAGE	COVER PAGE	01
POWER TREE	POWER TREE	02
POWER	VCC5P0,VCC3P3,VCC2P5,VCC1P8,VCC0P9	03
FPGA	GW5A-LV25LQ144	04-05
USB-JTAG	USB-JTAG	06

PCB DIAGRAM

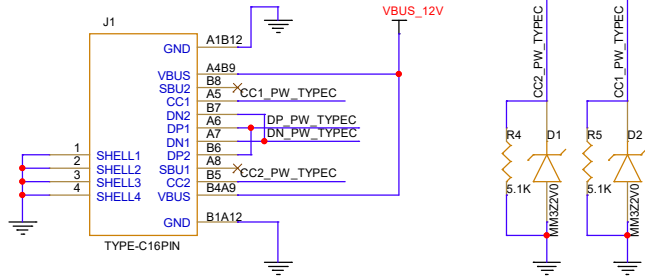


POWER TREE

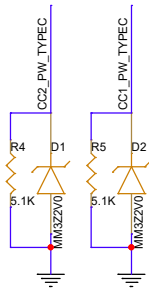


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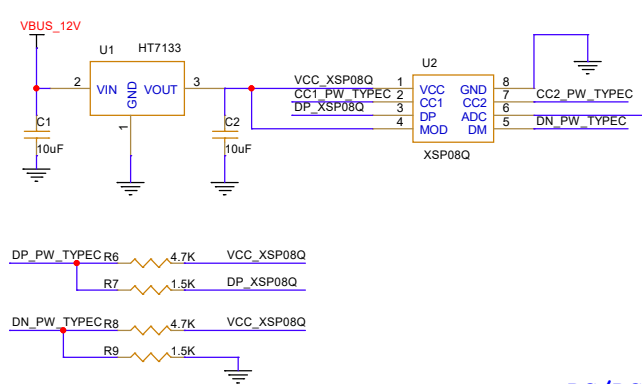
D

5
POWER IN
(Type-C)

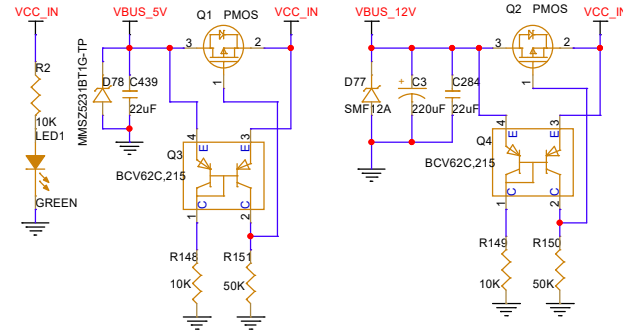
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3

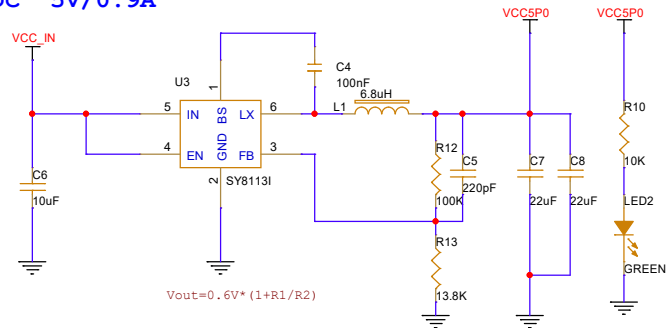


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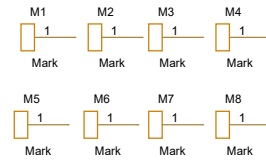


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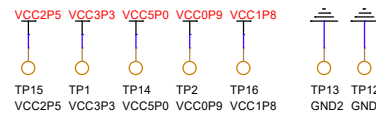
DC/DC 5V/0.9A



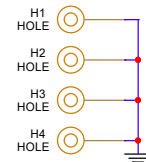
Mark



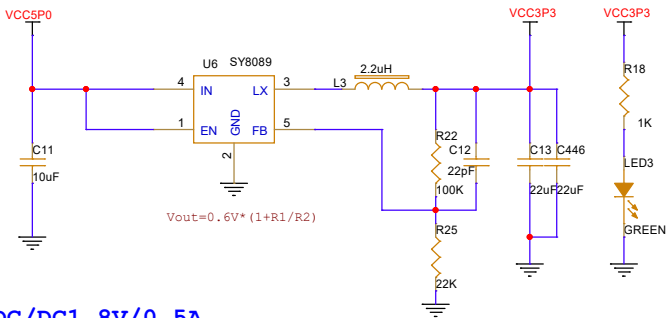
Power Test Hole



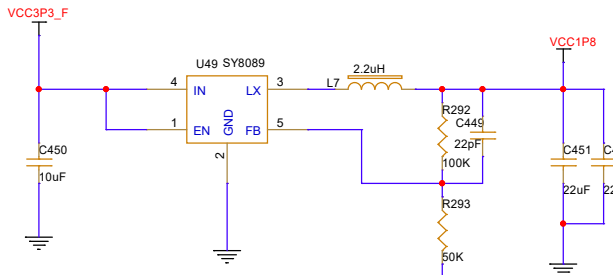
Mounting Hole



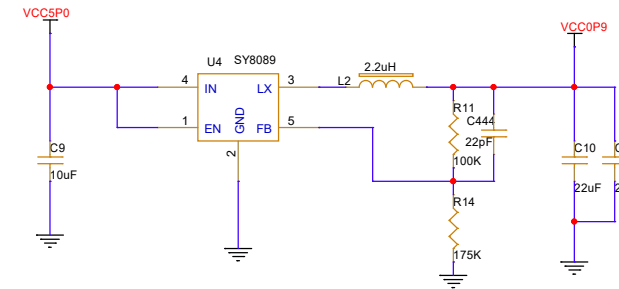
DC/DC 3.3V/0.8A



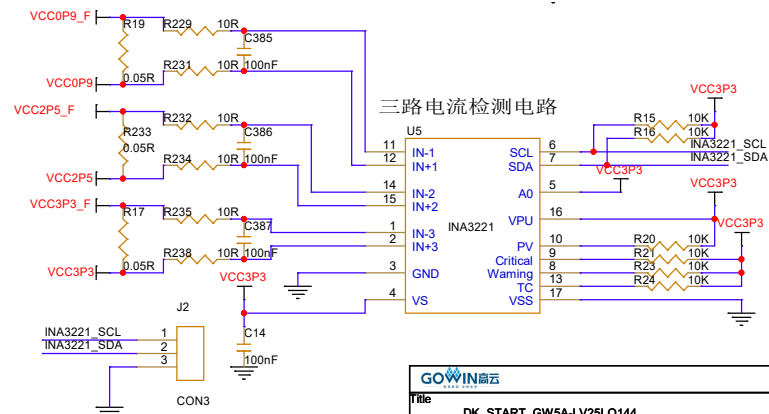
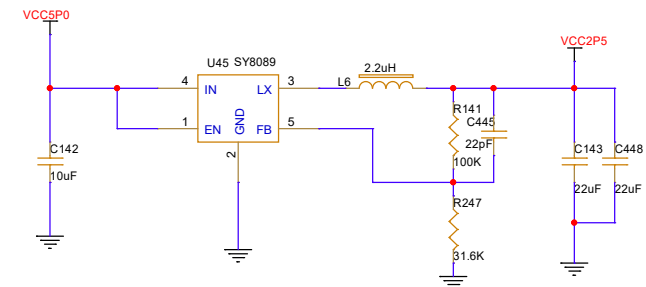
DC/DC1.8V/0.5A

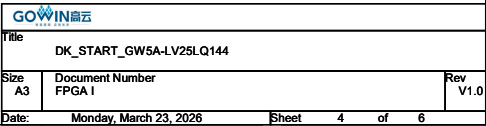


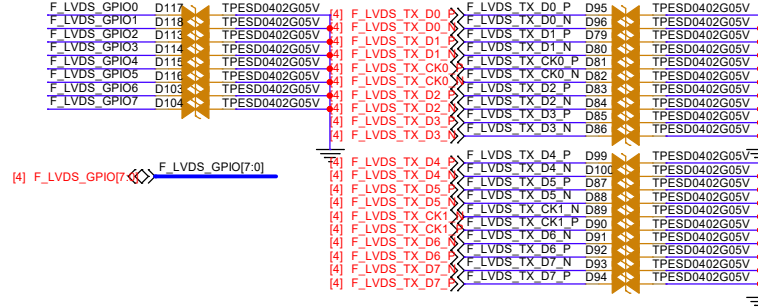
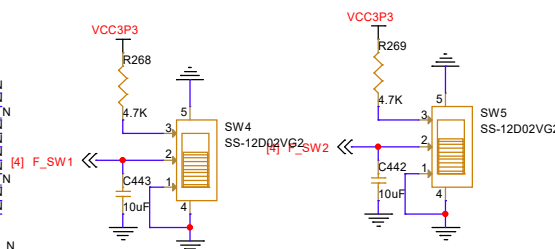
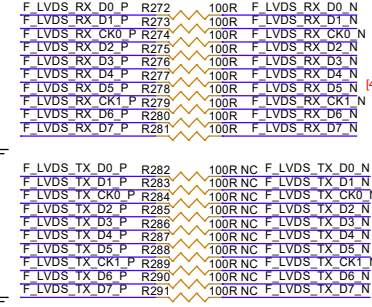
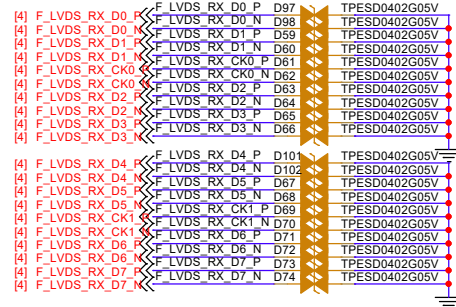
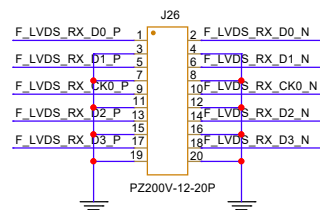
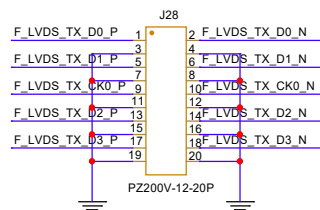
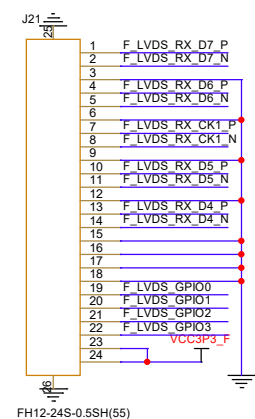
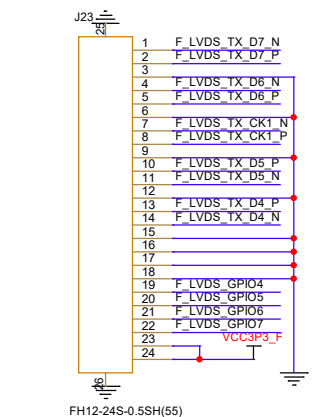
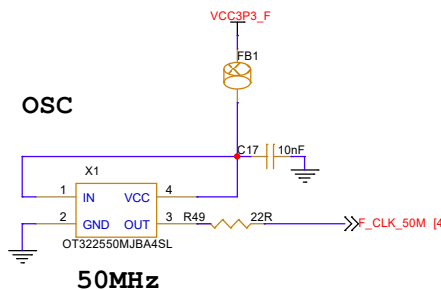
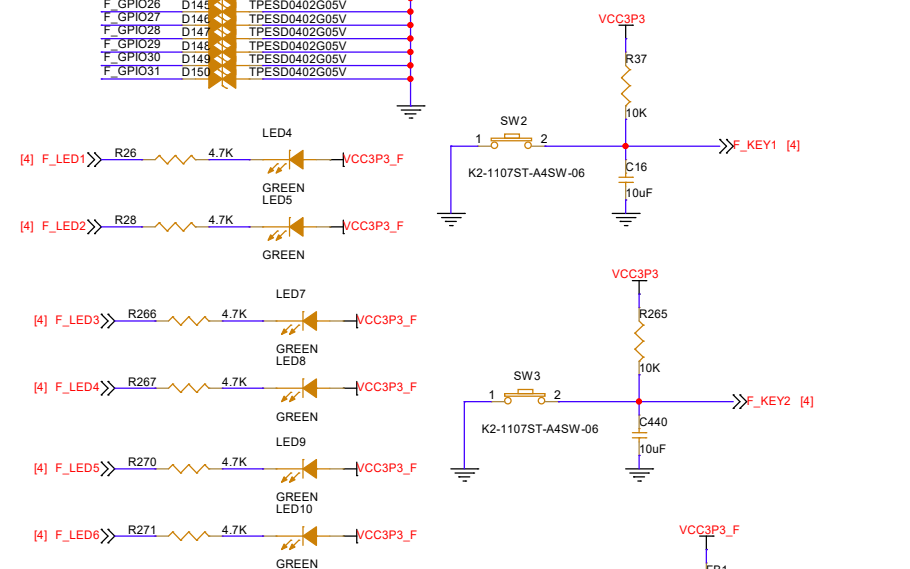
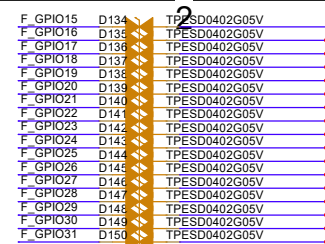
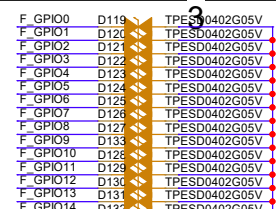
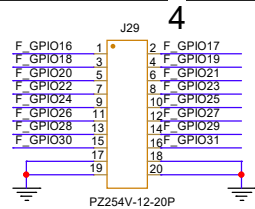
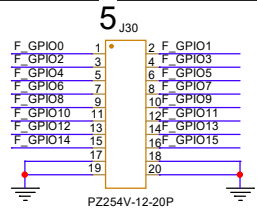
DC/DC 0.9V/1A



DC/DC 2.5V/0.5A







USB<->JTAG (FPGA Download)

Type-C

