



Gowin DDR3 Reference Design

Release Note

RN281-1.0E, 10/24/2018

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Revision History

| Date | Version | Description |
|------------|---------|----------------------------|
| 10/24/2018 | 1.0E | Initial version published. |

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About This Release

This release includes the Gowin DDR3 reference design.

Gowin DDR3 reference design is available at Gowin Website. It can be used for simulation and instantiation of the overall synthesis, placement and routing after user design.

Feature and Enhancement Summary

The released IP functions and enhancement items are as follows:

| Function | Description |
|--|--|
| Front-end synthesis tool: SynplifyPro, N-2018.03G-Beta7 | |
| Support DDR3 | Data read/write, etc. |
| Back-end place and routing tool: GoWinYunYuan_V1.8.1Beta | |
| Enhancements | – |
| Devices Supported | <ul style="list-style-type: none">● GW2A-18● GW2A-55● GW2AR-18 |

Platform Supported

The corresponding software of the released IPs supports the following platforms:

| | |
|---------|---------------------------------------|
| Windows | Windows 7 Ultimate edition (64-bit) |
| Linux | Red hat Enterprise Linux 6/7 (64-bit) |

Document

The released IP manuals are listed in the table below. You can download the PDF manuals at Gowin website or read online.

| Document | Access |
|--|-------------|
| Gowin DDR3 User Guide | Online, PDF |
| Gowin DDR3 Reference Design Release Note | Online, PDF |

