



Gowin Triple Speed Ethernet MAC

Release Note

RN538-1.0E, 2/14/2019

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Revision History

Date	Version	Description
2/14/2019	1.0E	1. IP Core Generator supporting Triple Speed Ethernet MAC; 2. Triple Speed Ethernet MAC reference design released; 3. User guide released.

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About This Release

This release includes Gowin Triple Speed Ethernet MAC user guide and reference design.

Gowin Triple Speed Ethernet MAC IP user guide and reference design are available at Gowin Website. Take specific parameters as an example for the reference design. They can be used for simulation and instantiation of the overall synthesis, placement and routing after user design.

Overview

Gowin Triple Speed Ethernet MAC implements the Ethernet MAC layer for receiving and sending Ethernet data.

Figure 1 Gowin Triple Speed Ethernet MAC IPOverview

Gowin Triple Speed Ethernet MAC IP	
IP Core Application	
Supported Devices	GW1N series (except GW1N-1) ,GW1NR series,GW2A series, GW2AR series
Delivered Doc.	
Design Files	Verilog (encrypted)
Reference Design	Verilog
TestBench	Verilog
Test and Design Flow	
Synthesis Software	Synplify_Pro
Application Software	GowinYunYuan

Document

This document release includes Gowin Triple Speed Ethernet MAC IP usage guide, as listed below:

Table -2 Gowin Triple Speed Ethernet MAC IP Guide

Documents	
Gowin Triple Speed Ethernet MAC IP User Guide	PDF
Gowin Triple Speed Ethernet MAC IP Release Notef	PDF

IP Support

Since the requirements of user design and the highest frequency are different, the reference design does not include associated FPGA specific location (.cst) and timing constraints (.sdc) documents, which the user can determine as required.

For the customized IP design and support, please contact Gowin sales and support line.

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