



Gowin CIC Filter IP

Release Note

RN797-1.0E, 07/28/2022

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Revision History

Date	Version	Description
07/28/2022	1.0E	Initial version published.

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1 About This Guide

This release includes CIC Filter IP User Guide.

Gowin CIC Filter IP User Guide is available at Gowin website, and the reference design has been configured with specific parameters that can be used for simulation and overall synthesis, placement and routing after instantiation with user design.

2 Overview

Gowin CIC Filter IP can implement integer division, output quotients and remainders.

Table 1 Gowin CIC Filter IP Overview

Gowin CIC Filter IP	
Delivered Doc.	
Design Files	Verilog (encrypted)
Reference Design	Verilog
TestBench	Verilog
Test and Design Flow	
Synthesis Software	GowinSynthesis
Application Software	Gowin Software (V1.9.8.07 and above)

3 Documents

The software release file contains Gowin CIC Filter IP User Guide and Release Note, which are listed below.

Table 2 Gowin CIC Filter IP Documents

Documents	Description
IPUG797, Gowin CIC Filter IP User Guide	PDF
RN797, Gowin CIC Filter IP Release Note	PDF

4 IP Support

Since the requirements of user design and the maximum frequency are different, the reference design does not include FPGA specific location (.cst) and timing constraints (.sdc) documents, and you can determine the location and timing constraints on your own.

For the customized IP design and support, please contact Gowin sales and support line.

Website: www.gowinsemi.com

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